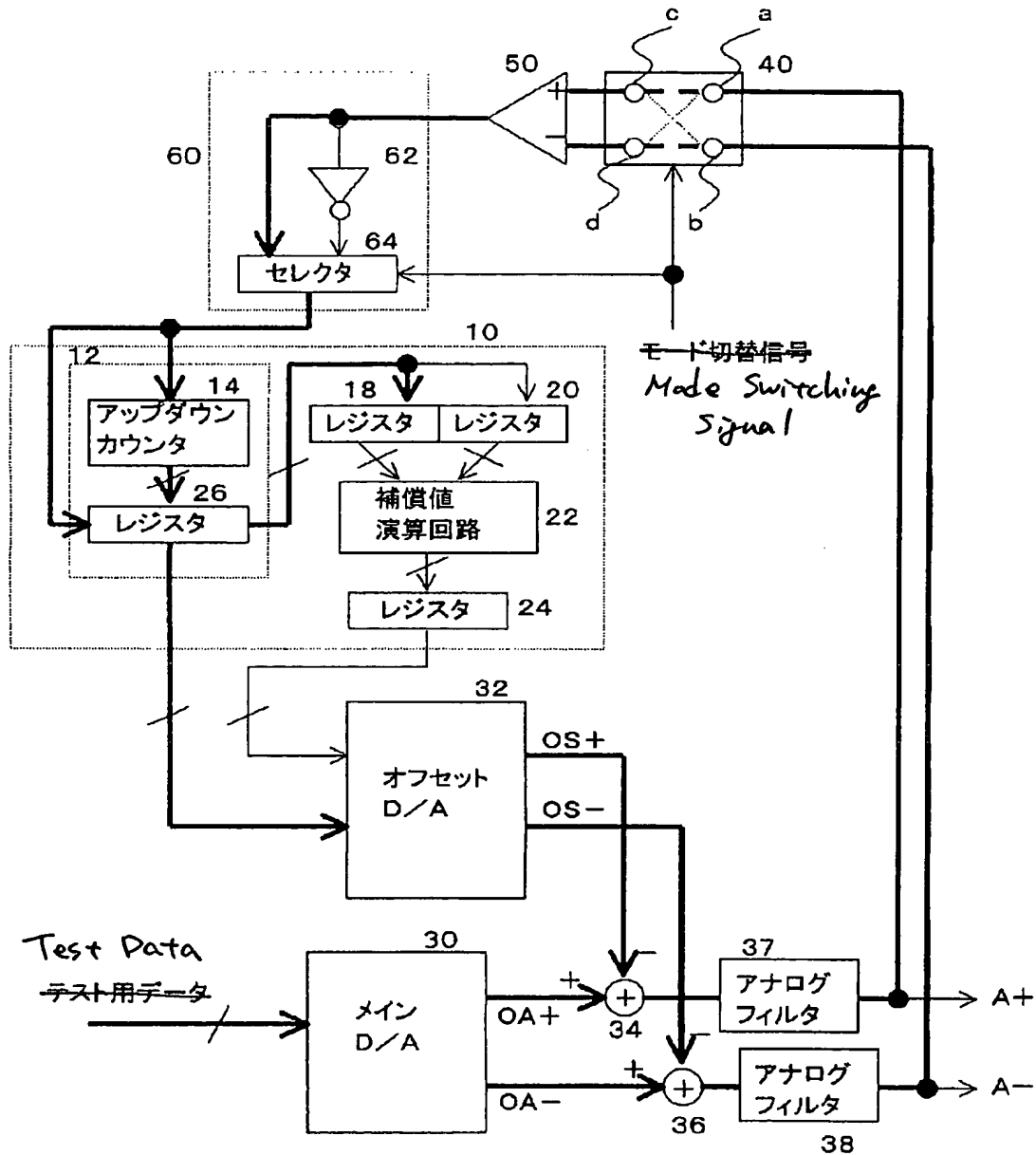


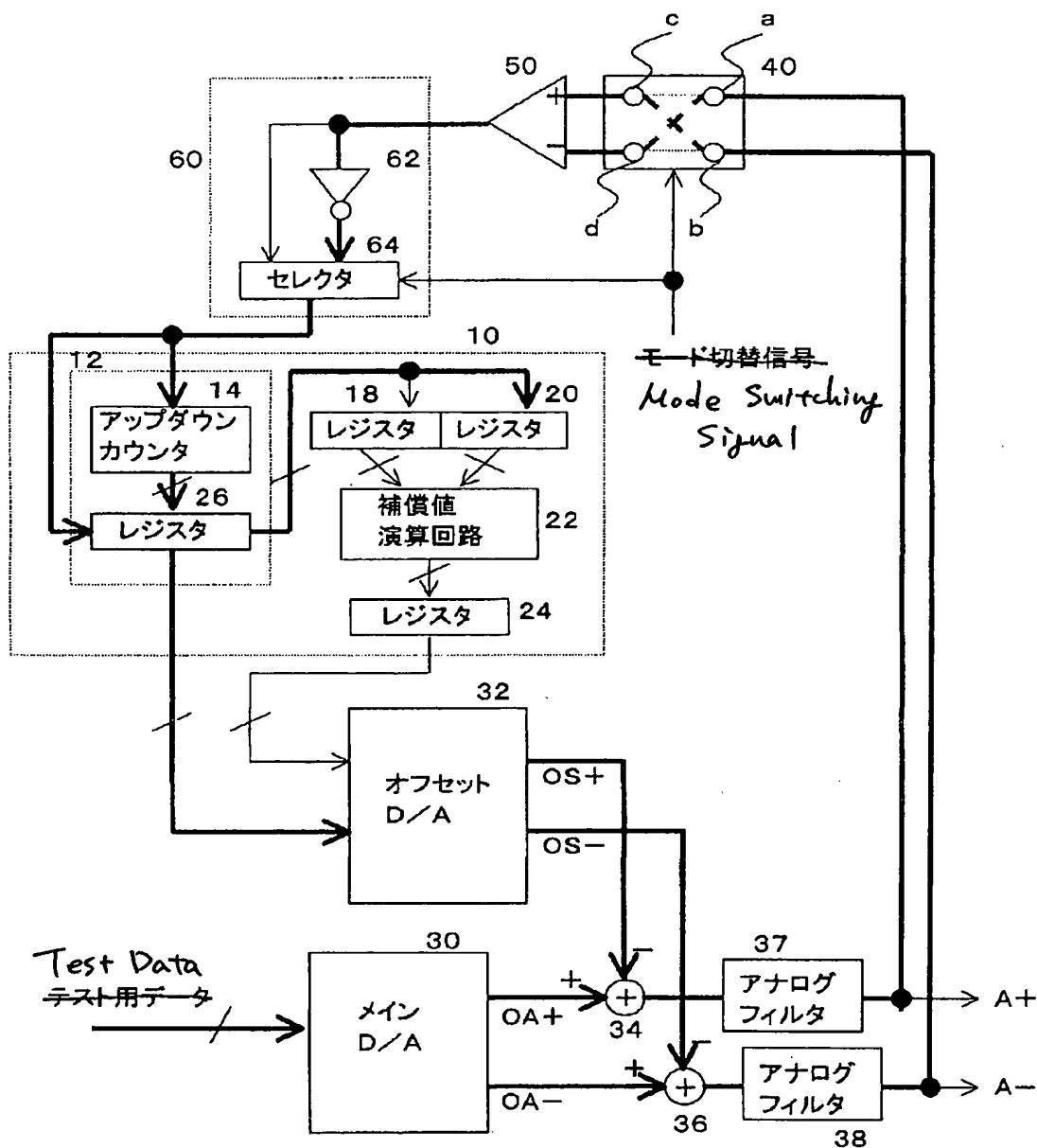
~~【書類名】~~ ~~図面~~

~~【図1】~~ FIG. 1



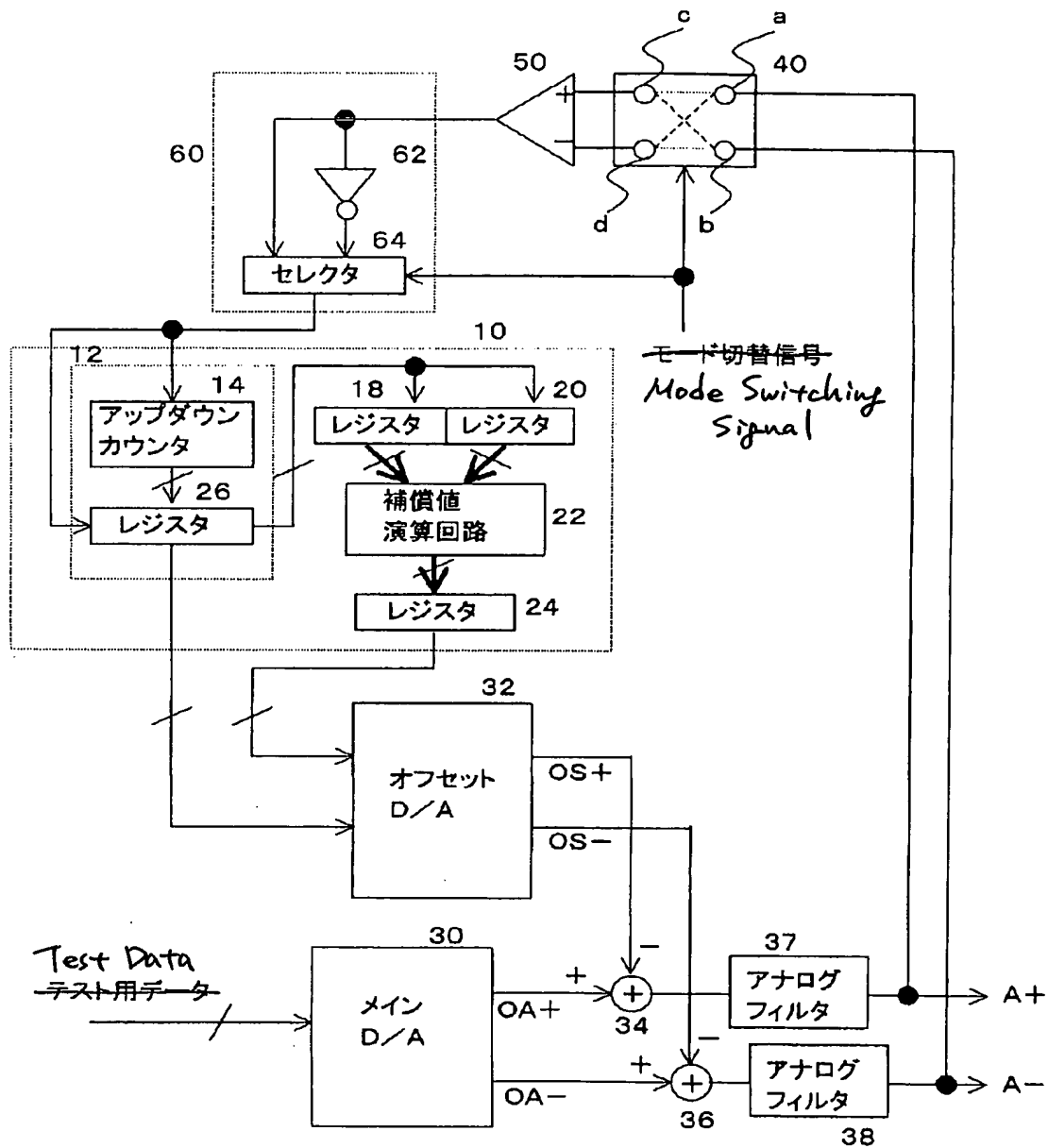
- 14 Up-Down Counter
- 18, 20 Register
- 22 Compensation Value Calculating Circuit
- 24, 26 Register
- 30 Main D/A Converter
- 32 Offset Compensation D/A Converter
- 37, 38 Analog Filter
- 64 Selector

FIG. 2



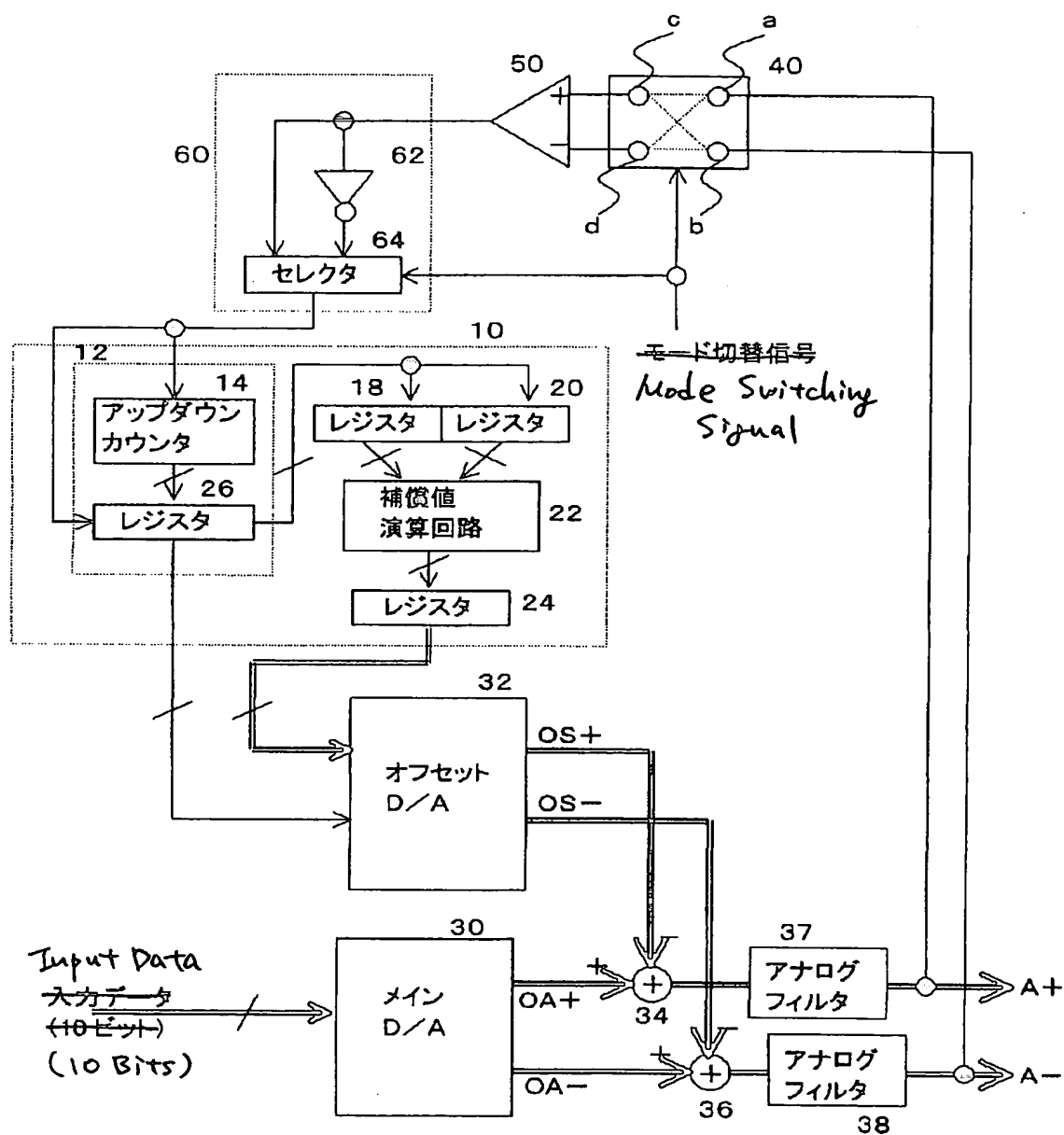
- 14 Up-Down Counter
- 18, 20 Register
- 22 Compensation Value Calculating Circuit
- 24, 26 Register
- 30 Main D/A Converter
- 32 Offset Compensation D/A Converter
- 37, 38 Analog Filter
- 64 Selector

**FIG. 3**



- 14 Up-Down Counter
- 18, 20 Register
- 22 Compensation Value Calculating Circuit
- 24, 26 Register
- 30 Main D/A Converter
- 32 Offset Compensation D/A Converter
- 37, 38 Analog Filter
- 64 Selector

~~FIG. 1~~ FIG. 4



- 14 Up-Down Counter
- 18, 20 Register
- 22 Compensation Value Calculating Circuit
- 24, 26 Register
- 30 Main D/A Converter
- 32 Offset Compensation D/A Converter
- 37, 38 Analog Filter
- 64 Selector

~~(a)~~

FIG. 5A

~~(a)~~

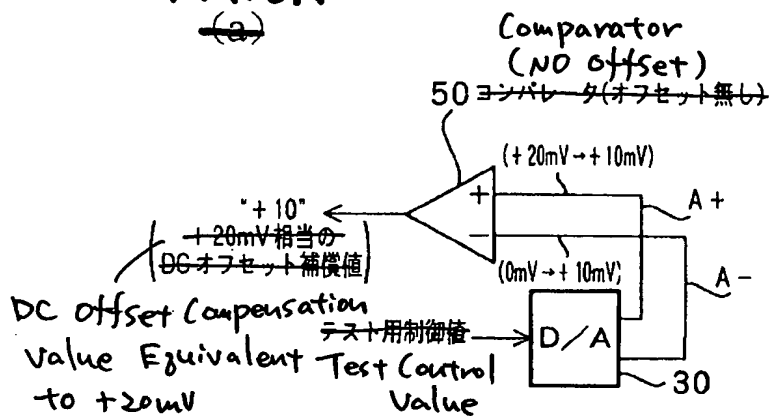


FIG. 5B

~~(b)~~

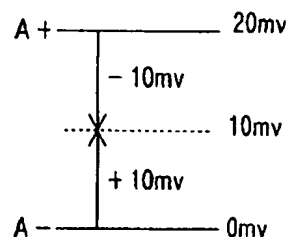


FIG. 5C

~~(c)~~

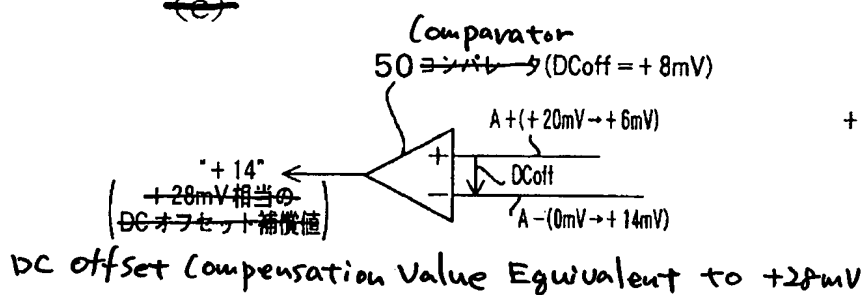
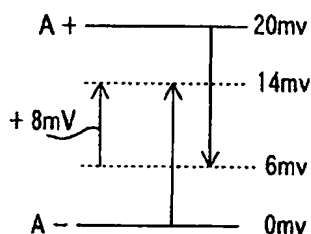
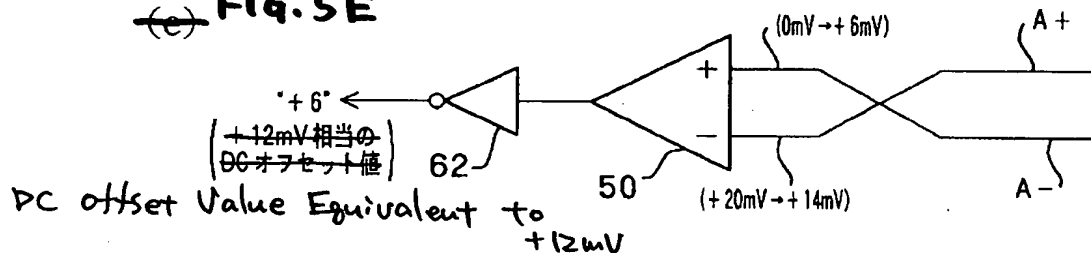


FIG. 5D

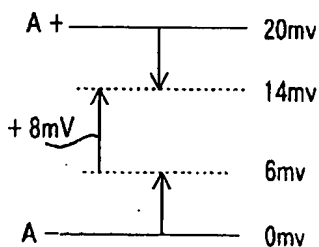
~~(d)~~



~~(e)~~ FIG. 5E



~~(f)~~ FIG. 5F



~~(g)~~ FIG. 5G

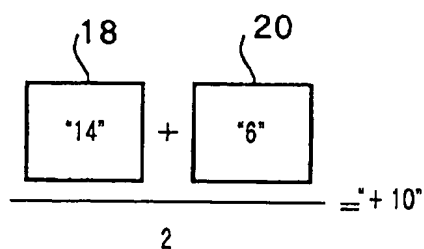
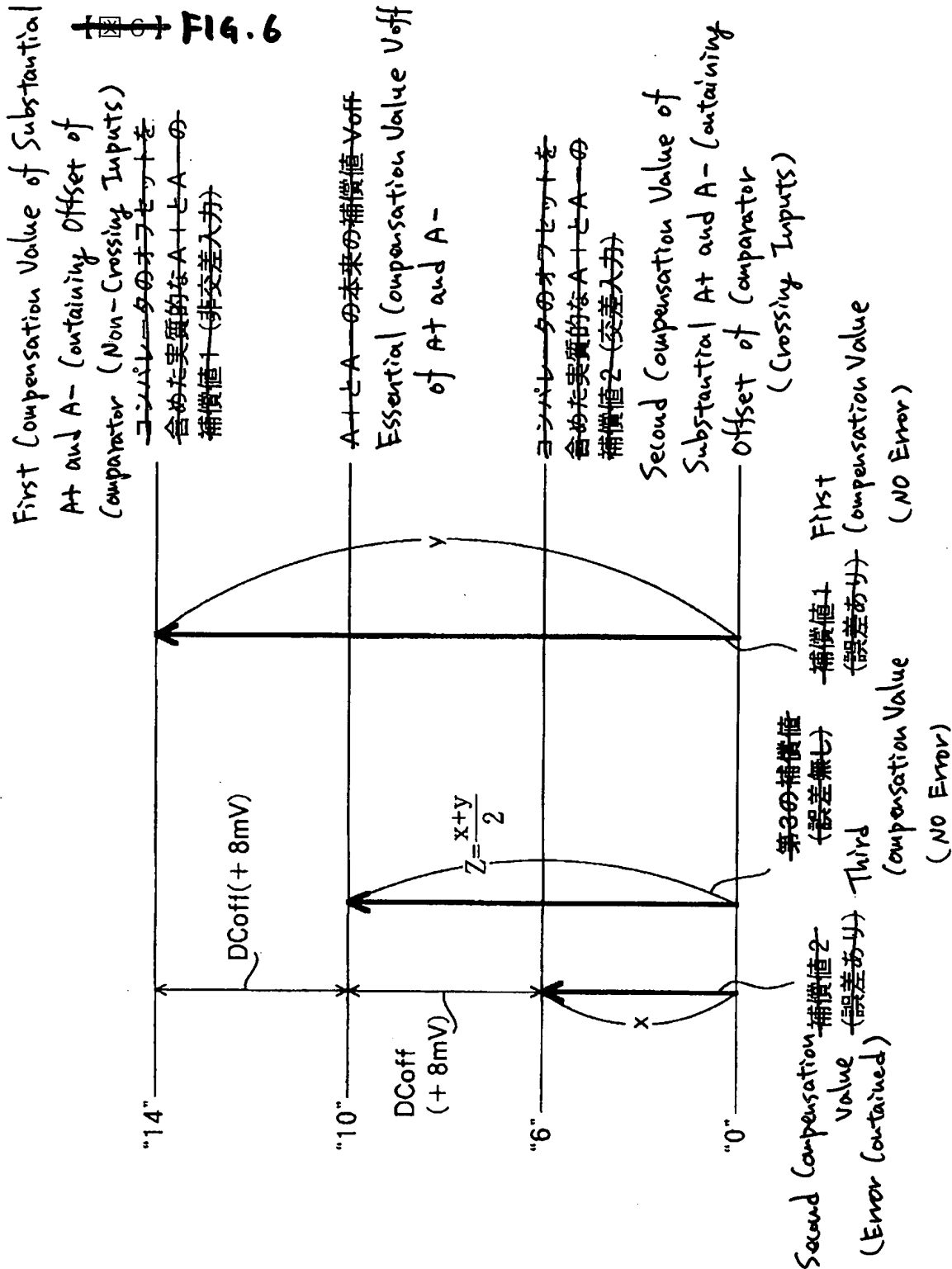
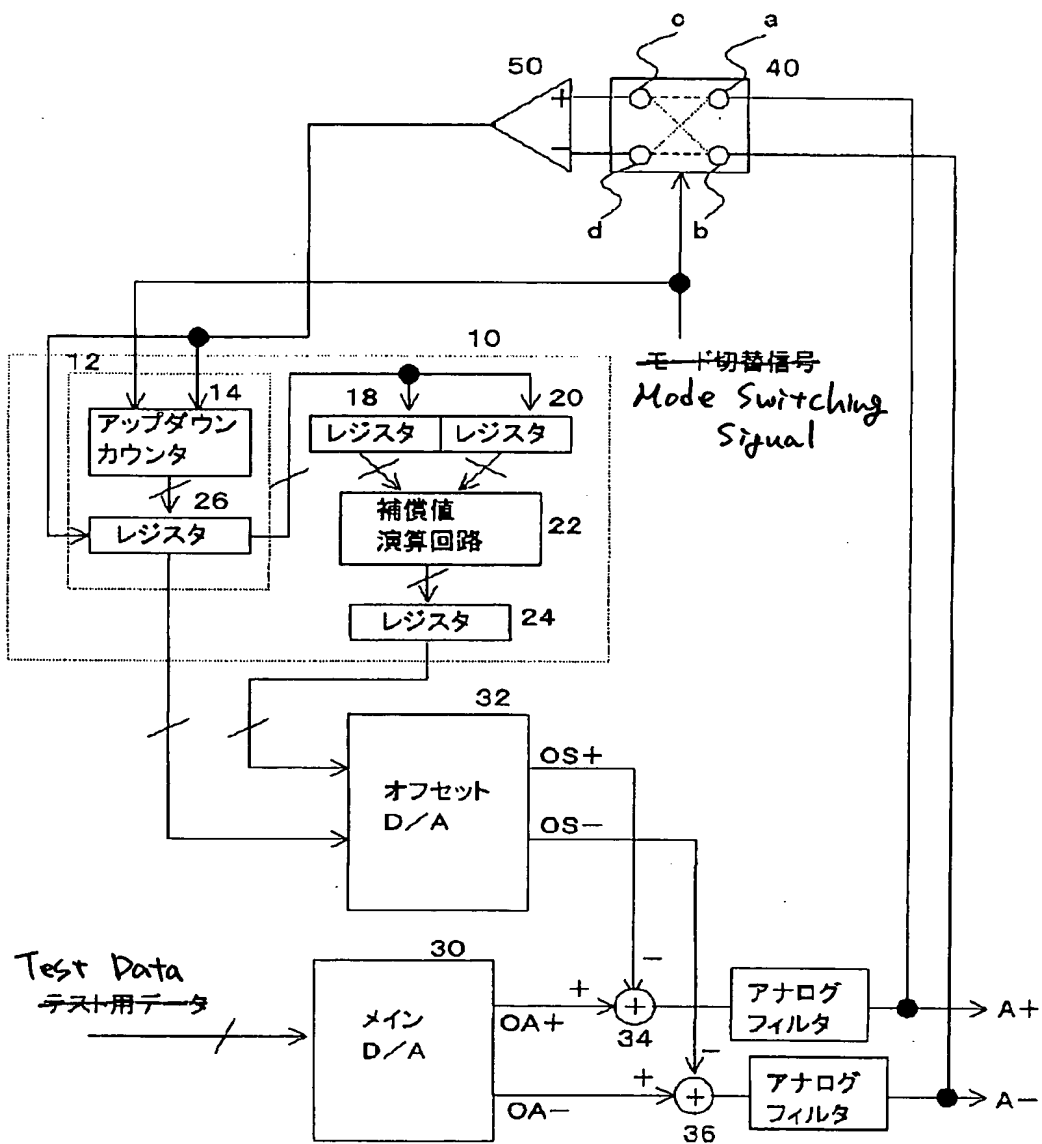


Fig. 6

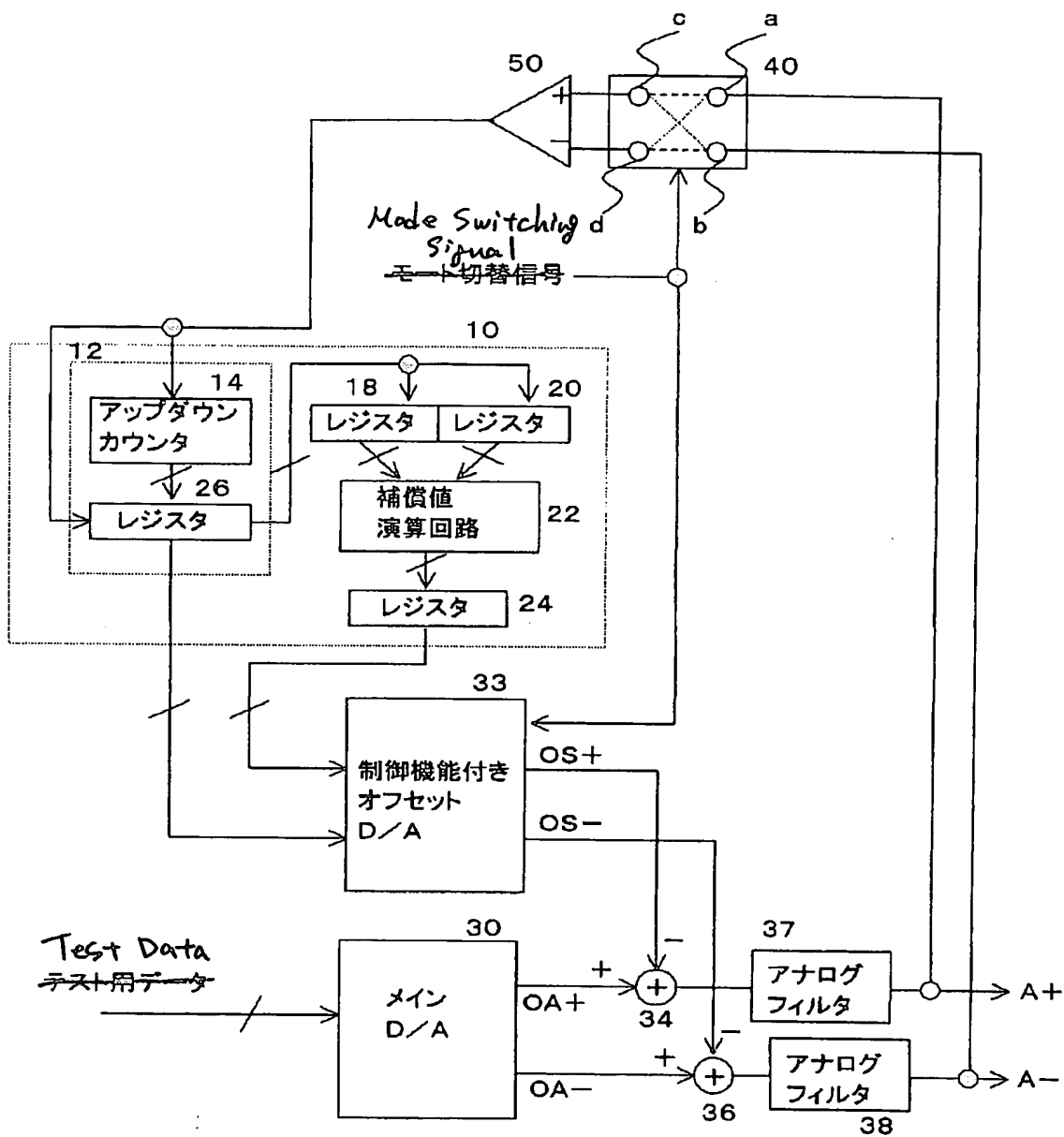


~~図7~~ FIG.7



- 14 Up-Down Counter
- 18, 20 Register
- 22 Compensation Value Calculating Circuit
- 24, 26 Register
- 30 Main D/A Converter
- 32 Offset Compensation D/A Converter
- 37, 38 Analog Filter

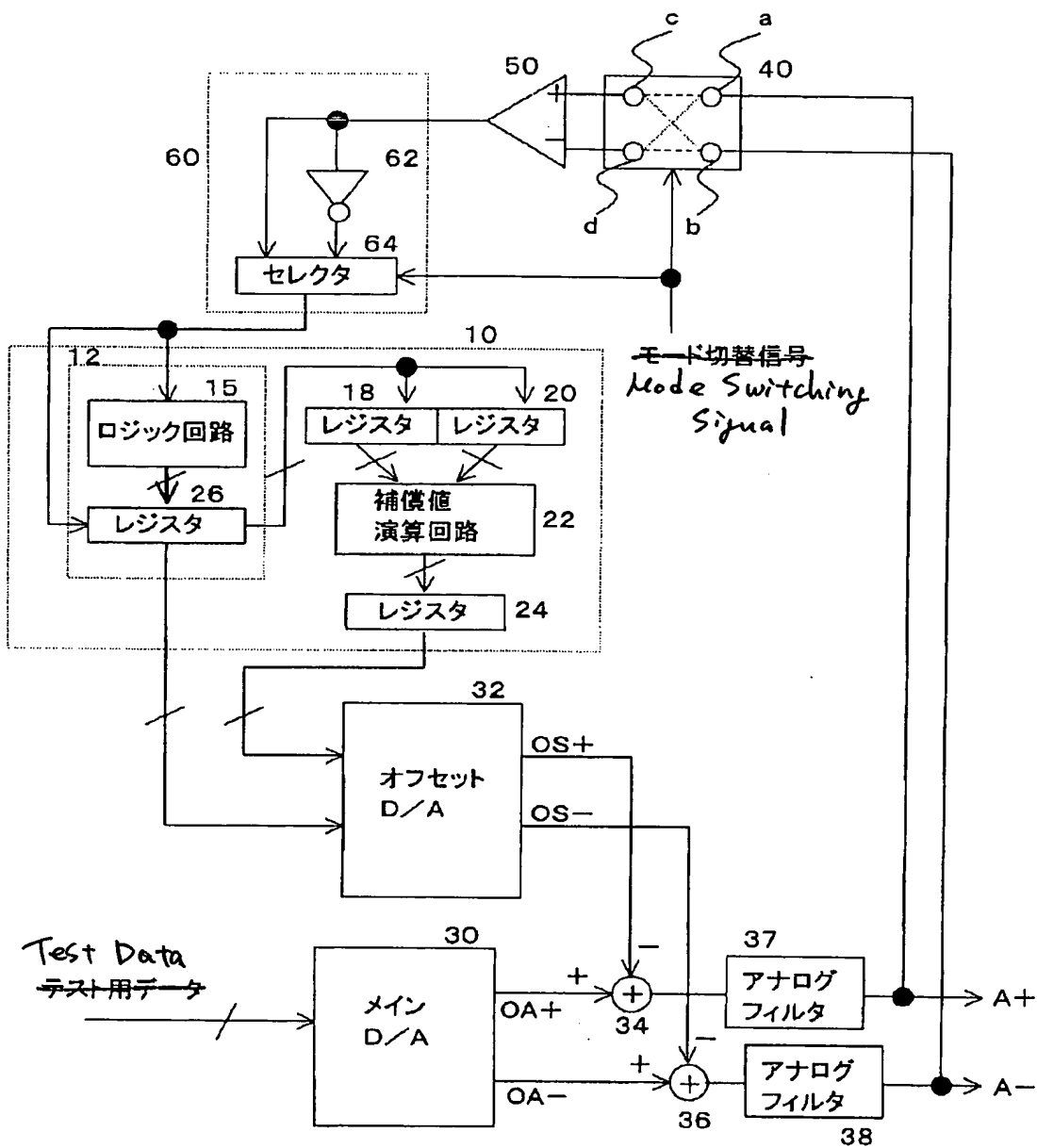
~~FIG. 8~~ FIG. 8



- |        |   |
|--------|---|
| 14     | Up-Down Counter   |
| 18, 20 | Register  |
| 22     | Compensation Value Calculating Circuit                      |
| 24, 26 | Register  |
| 30     | Main D/A Converter  |
| 33     | Offset Compensation D/A Converter with Controlling Function |
| 37, 38 | Analog Filter   |



~~FIG. 9~~ FIG. 9

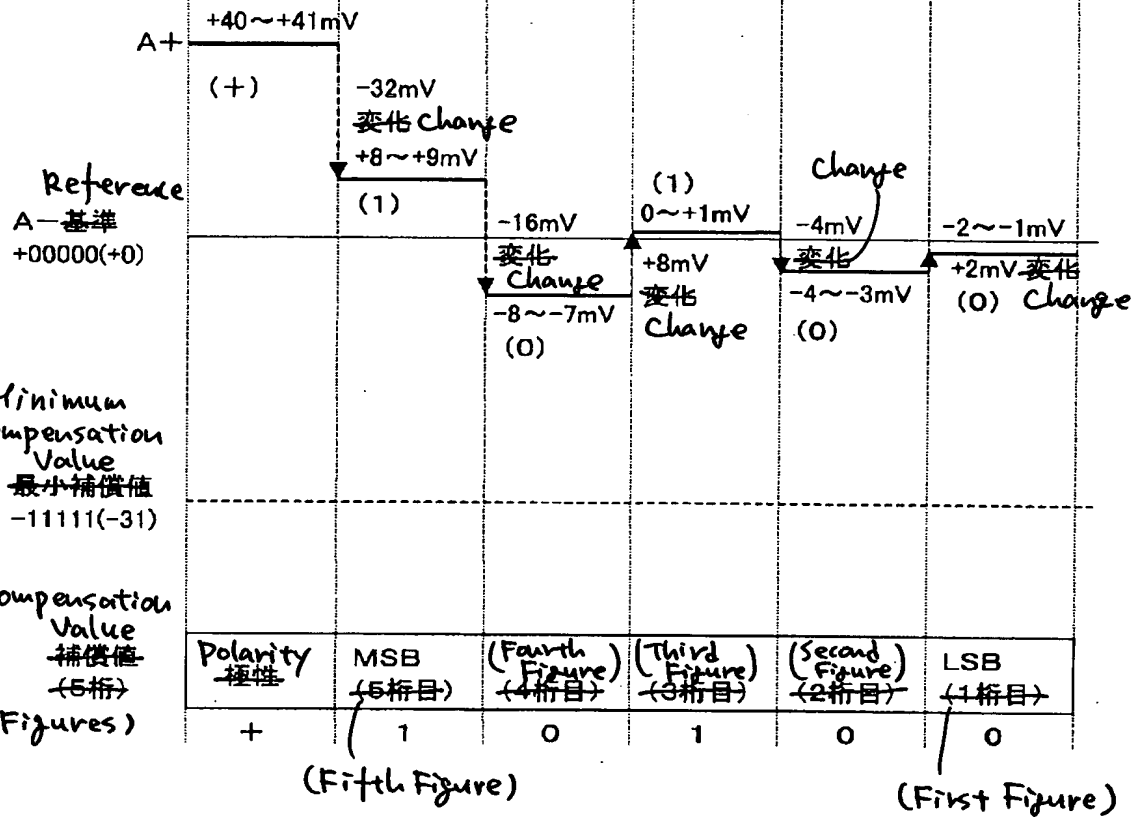


- 15 Logic Circuit
- 18, 20 Register
- 22 Compensation Value Calculating Circuit
- 24, 26 Register
- 30 Main D/A Converter
- 32 Offset Compensation D/A Converter
- 37, 38 Analog Filter
- 64 Selector

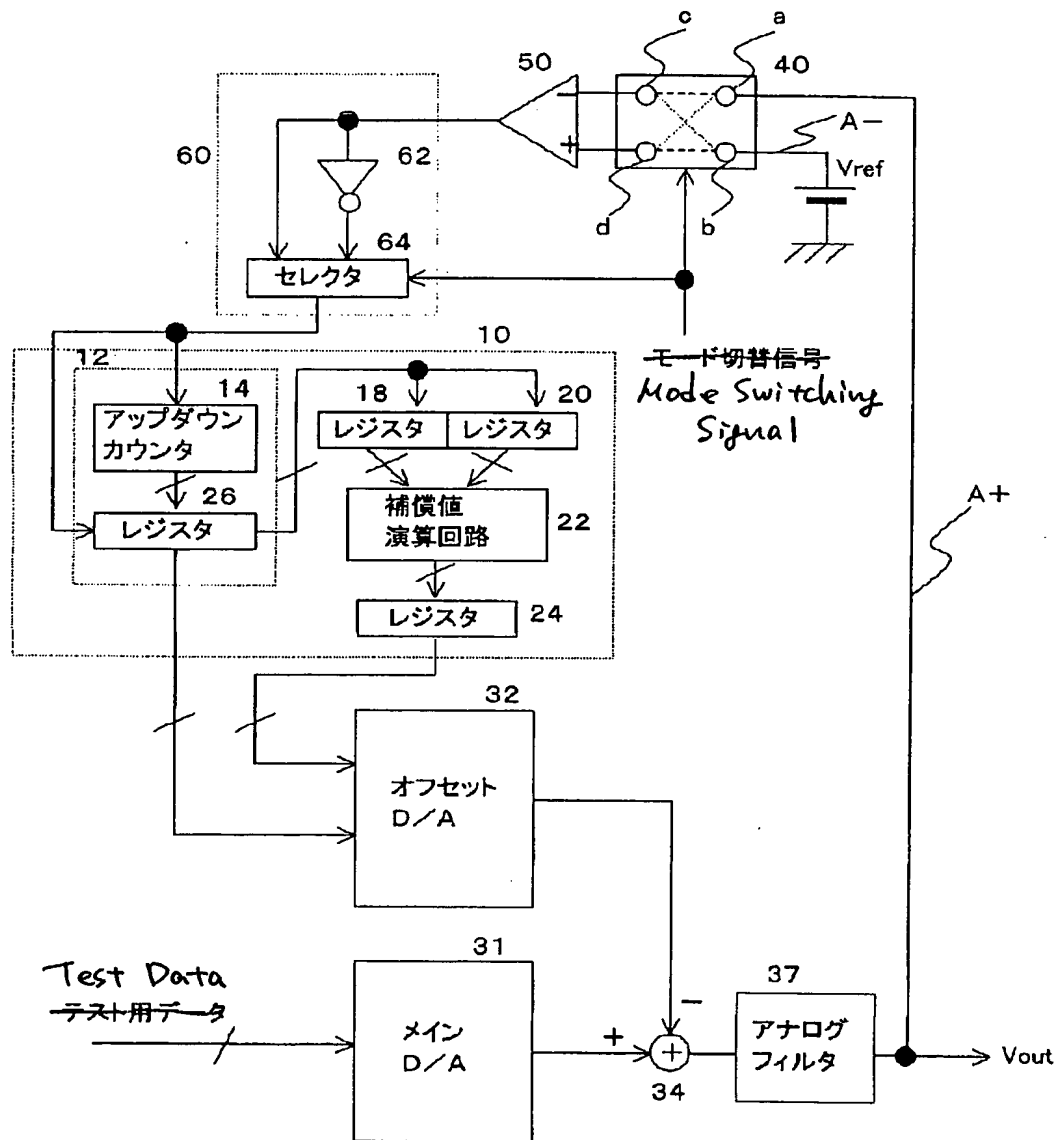
~~図10~~ FIG.10

Maximum Compensation Value

最大補償値  
+11111(+31)



~~FIG. 11~~ FIG. 11



- 14 Up-Down Counter
- 18, 20 Register
- 22 Compensation Value Calculating Circuit
- 24, 26 Register
- 31 Main D/A Converter
- 32 Offset Compensation D/A Converter
- 37 Analog Filter
- 64 Selector

~~【図 12】~~

FIG. 12A

~~(a)~~

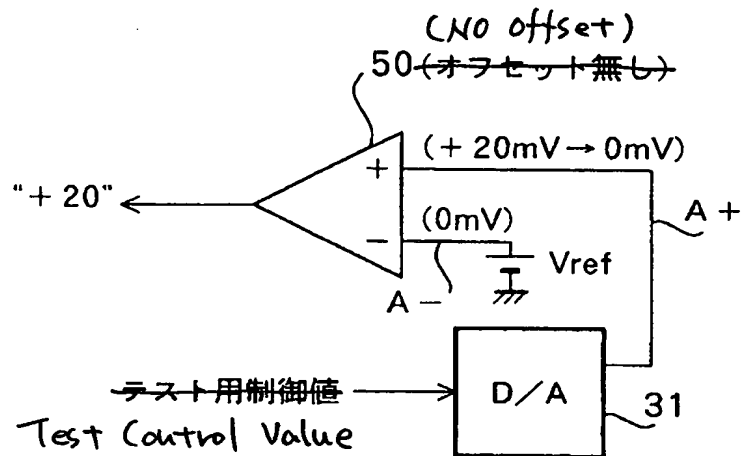


FIG. 12B

~~(b)~~

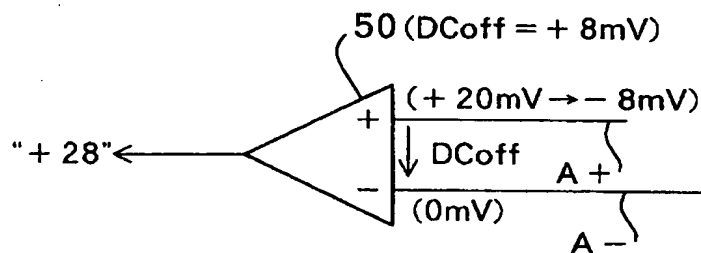


FIG. 12C

~~(c)~~

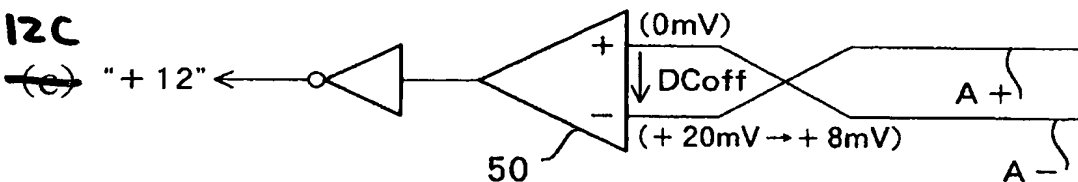
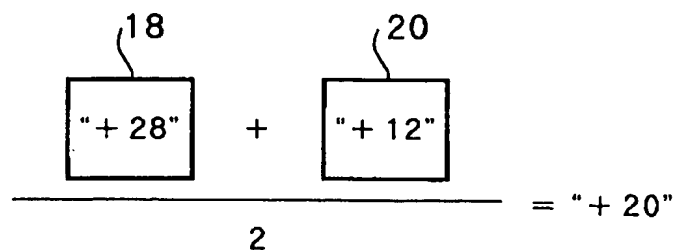
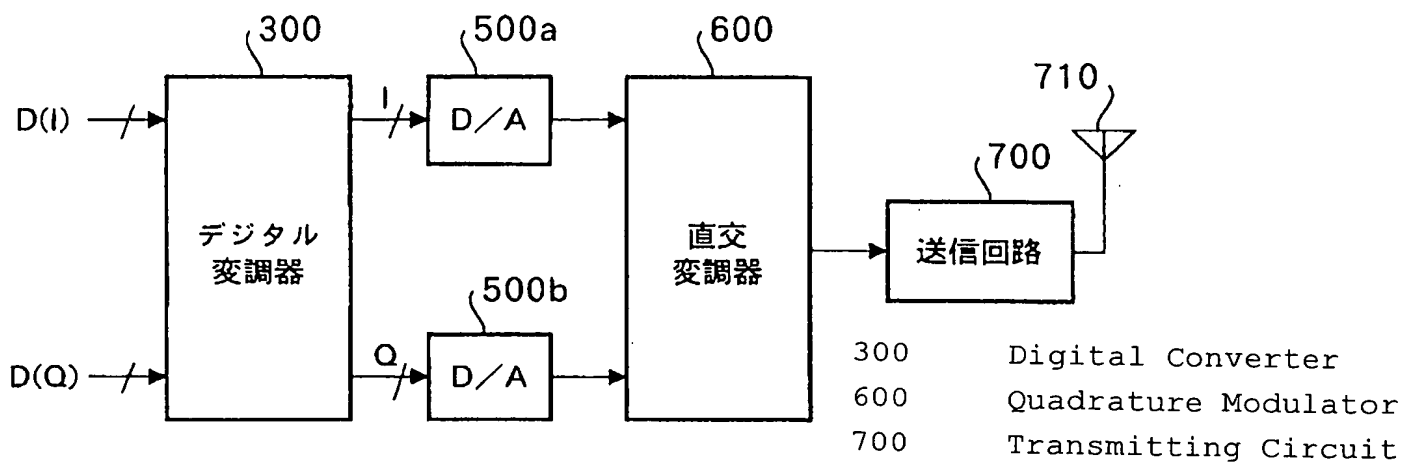


FIG. 12D

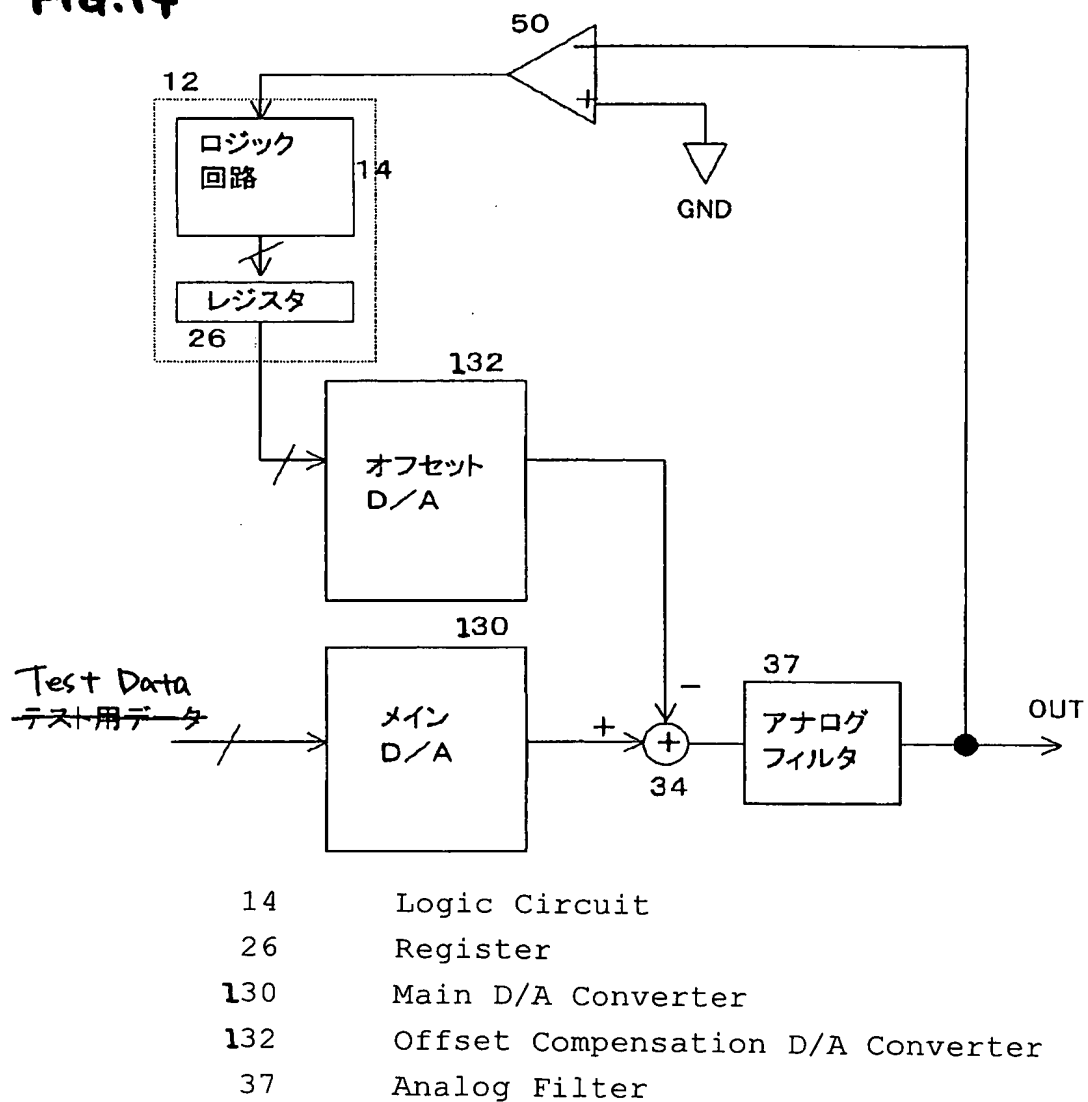
~~(d)~~



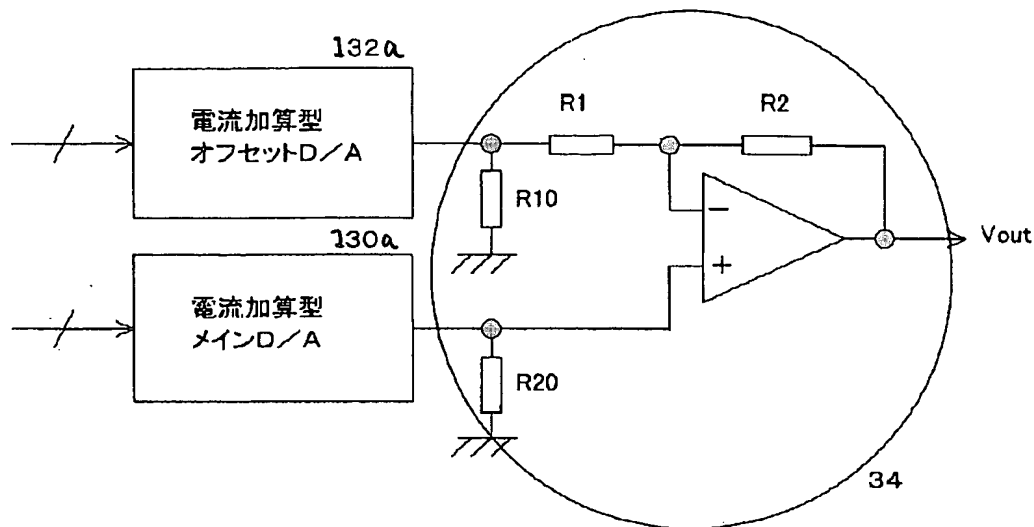
~~図 13~~ FIG.13



~~図 14~~ FIG.14



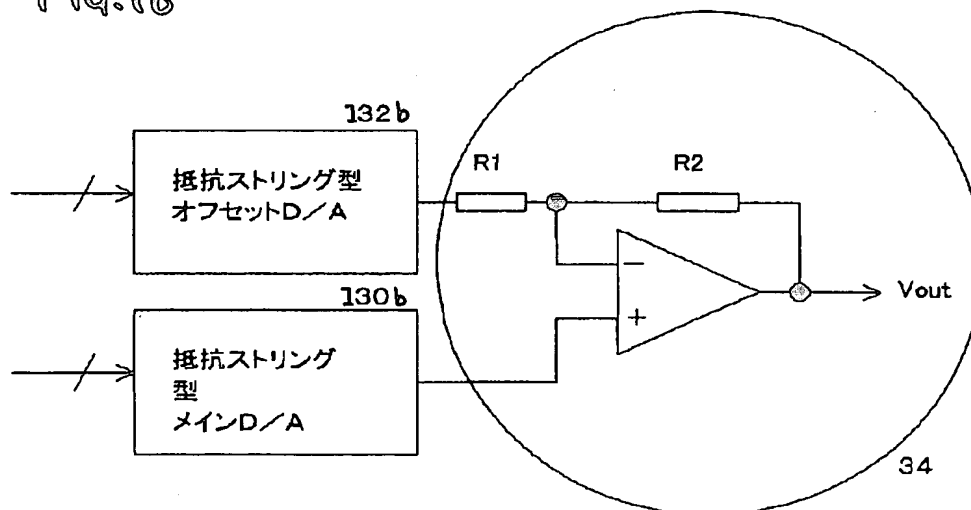
~~FIG. 15~~ FIG.15



130a Current Summation Type Main D/A Converter

~~FIG. 16~~ 132a Current Summation Type Offset Compensation D/A Converter

FIG.16



130b Resistor String Type Main D/A Converter

132b Resistor String Type Offset Compensation D/A Converter